



PATENT
ATTORNEY DOCKET NO.: 041993-5151

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:)
)
Seong-Kweon HA et al.)
)
Application No.: 09/709,523) Group Art Unit: 2814
)
Filed: November 13, 2000) Examiner: D. Farahani
)
For: SEMICONDUCTOR PACKAGE AND)
FABRICATION METHOD THEREOF)

Commissioner for Patents
Washington, D.C. 20231

Sir:

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AMENDMENT TRANSMITTAL FORM

1. Transmitted herewith is an Amendment responding to the Office Action mailed on May 3, 2002.
2. Additional papers enclosed:

- ☐ Drawings: ☐ Formal ☐ Informal (Correction)
- ☐ Information Disclosure Statement
- ☐ Form PTO-1449, _____ references included
- ☐ Citations
- ☐ Declaration of Biological Deposit
- ☐ Submission of "Sequence Listing", computer readable copy and/or amendment pertaining thereto for biotechnology invention containing nucleotide and/or amino acid sequence.

3. Extension of Time

The proceedings herein are for a patent application and the provisions of 37 C.F.R. § 1.136(a) apply.

☒ Applicant believes that no extension of time is required. However, this conditional petition is being made to provide for the possibility that applicant has inadvertently overlooked the need for a petition and fee for extension of time.

☐ Applicant petitions for an extension of time, the fees for which are set out in 37 C.F.R. § 1.17(a), for the total number of months checked below:

<u>Total Months Requested</u>	<u>Fee for Extension</u>	<u>[Fee for Small Entity]</u>
<input type="checkbox"/> one month	\$ 110.00	\$ 55.00
<input type="checkbox"/> two months	\$ 400.00	\$ 200.00
<input type="checkbox"/> three months	\$ 920.00	\$ 460.00
<input type="checkbox"/> four months	\$ 1,440.00	\$ 720.00

Extension of time fee due with this request: \$_____

If an additional extension of time is required, please consider this a Petition therefor.

☐ An extension for _____ months has already been secured and the fee paid therefor of \$_____ is deducted from the total fee due for the total months of extension now requested.

4. Constructive Petition

☒ **EXCEPT** for issue fees payable under 37 C.F.R. § 1.18, the Commissioner is hereby authorized by this paper to charge any additional fees during the entire pendency of this application including fees due under 37 C.F.R. §§ 1.16 and 1.17 which may be required, including any required extension of time fees, or credit any overpayment to Deposit Account 50-0310. This paragraph is intended to be a **CONSTRUCTIVE PETITION FOR EXTENSION OF TIME** in accordance with 37 C.F.R. § 1.136(a)(3).

5. Fee Calculation (37 C.F.R. §1.16)

CLAIMS AS AMENDED						
	Claims Remaining After Amendment		Highest No. Previously Paid	Present Extra	at Rate of	Total Fees
Total Claims (37 C.F.R. §1.16(c))	9	minus	20	0	x \$18 each =	+ \$ 0.00
Independent Claims (37 C.F.R. §1.16(b))	1	minus	3	0	x \$84 each =	+ \$ 0.00
[] First presentation of Multiple dependent claim(s)					\$280.00	+ \$ 0.00
SUB-TOTAL =						\$ 0.00
Reduction by ½ for filing by a small entity						- \$ 0.00
TOTAL FEE =						\$ 0.00

6. Fee Payment

- ☒ No fee is to be paid at this time.
- ☐ Check in the amount of \$_____ for _____. The Commissioner is hereby authorized to charge any additional extension of time fee or additional fee for claims due to Deposit Account No. 50-0310.
- ☒ The Commissioner is hereby authorized to charge any additional fees which may be required, including fees due under 37 C.F.R. §§ 1.16 and 1.17, or credit any overpayment to Deposit Account 50-0310.

Respectfully submitted,

MORGAN, LEWIS & BOCKIUS LLP

Dated: July 31, 2002

By: Goran P. Stojkovich
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Group Art Unit: 2817
Examiner: D. Farahani

Commissioner for Patents
Washington, D.C. 20231

**AMENDMENT AND REQUEST FOR
RECONSIDERATION UNDER 37 C.F.R. § 1.111**

Sir:

In response to the non-final Office Action mailed on May 3, 2002 (Paper No. 8), the period of response extending through August 3, 2002, please amend the above-identified application and reconsider the application in view of the Remarks that follow.

IN THE CLAIMS:

Please amend claim 2 as follows:

2. (Amended) A semiconductor package, comprising:
- a semiconductor substrate;
 - a plurality of chip pads separately formed on an upper surface of the semiconductor substrate;
 - a first metal pattern formed on upper surfaces of the plurality of chip pads;